

IN THE CLAIMS

1. (Currently amended) An electronic circuit comprising:

a plurality of sequential logic elements each comprising:

at least one clock terminal for receiving a clock signal;

at least one input terminal for receiving an input signal;

at least one output terminal for providing an output signal;

circuitry, for respective ones of the plurality of sequential logic elements, for monitoring respective ones of said input and output signals to provide respective analog control signals in response thereto; and means for combining said respective control signals to form a combined analog control signal and controlling a power consumption of the electronic circuit in response to said combined analog control signal.

2. (Currently amended) An electronic circuit as claimed in claim 1, characterized in that wherein it is capable of being controlled at a rate determined by the clock signal.

3. (Currently amended) An electronic circuit as claimed in claim 1 characterized in that it wherein is capable of providing information relating to future power consumption.

4. (Currently amended) An electronic circuit as claimed in claim 1, characterized by its ability of having wherein future power consumption being is controllable in advance based upon past logical events.

5. (Previously presented) An apparatus that includes an electronic circuit as claimed in

claim 1.

6. (Currently amended) A method of controlling power consumption of an electronic circuit that includes a plurality of sequential logic elements each comprising: at least one clock terminal for receiving a clock signal, at least one input terminal for receiving an input signal, and at least one output terminal for providing an output signal, the method comprising the steps of:

for respective ones of the plurality of sequential logic elements, monitoring respective ones of said input and output signals to provide respective analog control signals in response thereto; and

combining said respective control signals to form a combined analog control signal and controlling a power consumption of the electronic circuit in response to said combined analog control signal.